	Type	L #	Hits	Search Text	DBs
1	BRS	L1	267723	LCD or (liquid adj crystal adj display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	30817	(active adj matrix) or AMLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	51701	TFT or (thin adj film adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	29909	1 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	15403	2 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	17339	pixel adj electrode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	9485	(potential or voltage) and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	7145	7 and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	636	345/55.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	3908	(345/87-89).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	2531	(345/90-96).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	530	(345/208-209).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	6892	9 or 10 or 11 or 12	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	900	8 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	747	345/92.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
16	BRS	L16	257	14 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L17	28782	(signal adj lines) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L18	31	(polarity adj inver$4) and 17 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
19	BRS	L19	120665	phase adj shift$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L20	47270	PWM or (pulse adj width adj modulation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L21	49677	(common adj electrode) or (counter adj electrode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
22	BRS	L22	7	(conduction adj period) and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
23	BRS	L23	0	(on near state) and 6 and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
24	BRS	L24	231294	gradation or greyscale or grayscale or tone or halftone	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
25	BRS	L25	1568	7 and 24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
26	BRS	L27	17703	(drive adj pulse) or (applied adj voltage) and (19 or 20)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
27	BRS	L28	257	8 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
28	BRS	L29	79	24 and 28	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
29	BRS	L30	290	6 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
30	BRS	L31	83	24 and 30	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
31	BRS	L32	65	24 and 30	USPAT; EPO; JPO; DERWENT; IBM_TDB
32	BRS	L33	79	16 and 24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33	BRS	L34	61	24 and 28	USPAT; EPO; JPO; DERWENT; IBM_TDB
34	BRS	L35	6	6 and 19 and 15	USPAT; EPO; JPO; DERWENT; IBM_TDB
35	BRS	L36	1	(conduction adj period) and 13 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
36	BRS	L37	772	(power near reduc$4) and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
37	BRS	L38	464	21 and 37	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
38	BRS	L39	36	15 and 38	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
39	BRS	L40	33217	gate adj voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
40	BRS	L41	1283	(gate adj voltage) and 40 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
41	BRS	L42	220	41 and 37	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
42	BRS	L43	79	24 and 42	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	L44	73	40 and 6 and 15	USPAT; EPO; JPO; DERWENT; IBM_TDB
44	BRS	L45	93408	temperature near detect$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
45	BRS	L46	260	345/101.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
46	BRS	L47	117	349/72.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
47	BRS	L48	341	46 or 47	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
48	BRS	L50	593401	(operational adj amplifier) or amplifier or opamp	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
49	BRS	L51	1122	345/98.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
50	BRS	L52	887	345/100.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
51	BRS	L53	2529	345/87.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
52	BRS	L54	4026	51 or 52 or 53	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
53	BRS	L55	23117	level adj shift$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
54	BRS	L56	11	46 and 55	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
55	BRS	L57	413	54 and 55	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
56	BRS	L58	622	54 and 50	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
57	BRS	L59	6	45 and 50 and 51	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
58	BRS	L60	262	50 and 51	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
59	BRS	L61	6	45 and 60	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
60	BRS	L62	2818	(column adj driver) or (scanning adj driver)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
61	BRS	L63	218	55 and 62	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
62	BRS	L64	0	46 and 63	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
63	BRS	L65	159	55 and 62	USPAT; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	137756	waveforms or (timing adj diagram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	55641	(gate near voltage) or (gate adj pulse)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	1690	(345/92-94.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	71298	(source adj voltage) or (data adj pulse)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	39443	(common adj electrode) or (counter adj electrode) and (voltage or potential)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	405	1 and 4 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	49	3 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	3236	(345/89-96).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	91	6 and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	416	2 and 4 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	6021	(345/87-96).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	119	10 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	246308	(grey adj scale) or greyscale or (gray adj scale) or gradation or halftone or tone or half-tone	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	404	4 and 5 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	127	11 and 14	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
16	BRS	L16	77	11 and 14	USPAT; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L18	79	sharp and 4 and 5 and 1	USPAT; EPO; JPO; DERWENT; IBM_TDB

	Issue Date	Pages	Title	Document ID	Current OR
1	20030930	288	Liquid crystal display panel including a light shielding film to control incident light	US 6628355 B1	349/106
2	20030819	57	Electron-emitting device, electron source and image-forming apparatus as well as method of manufacturing the same	US 6608437 B1	313/495
3	20030708	24	Liquid crystal display device having stabilized pixel electrode potentials	US 6590550 B2	345/87
4	20030527	29	Method of driving liquid crystal display device	US 6570551 B2	345/89
5	20030107	29	Method of driving liquid crystal display device	US 6504521 B1	345/89
6	20021126	40	Liquid crystal display device, and method for driving the same	US 6486864 B1	345/92
7	20020820	32	Data signal line driving circuit and image display apparatus	US 6437768 B1	345/100
8	20020820	40	Electron source fabricating method and an image forming apparatus fabricating method	US 6435928 B1	445/24
9	20020806	47	Electrophysiological treatment methods and apparatus employing high voltage pulse to render tissue temporarily unresponsive	US 6428537 B1	606/41
10	20020716	47	Systems and methods for conducting electrophysiological testing using high-voltage energy pulses to stun tissue	US 6421556 B2	600/510
11	20020611	36	Level-shifting pass gate	US 6404230 B1	326/68

	Issue Date	Pages	Title	Document ID	Current OR
12	20020423	30	Static clock pulse generator and display	US 6377104 B2	327/291
13	20020409	48	Power supply for use in electrophysiological apparatus employing high-voltage pulses to render tissue temporarily unresponsive	US 6369465 B1	307/112
14	20020319	17	Voltage level shifter and poly-silicon display	US 6359491 B1	327/333
15	20020101	45	Electron-emitting device and electron source and image-forming apparatus using the same as well as method of manufacturing the same	US 6334801 B1	445/24
16	20011218	64	Differential amplifier, operational amplifier employing the same, and liquid crystal driving circuit incorporating the operational amplifier	US 6331846 B1	345/96
17	20011009	42	Method of manufacturing an electron source and image-forming apparatus using the electron source	US 6299497 B1	445/24
18	20010904	52	Electron source and image forming apparatus as well as method of providing the same with means for maintaining activated state thereof	US 6283815 B1	445/41
19	20010828	13	Display device and a method of addressing a display device	US 6281866 B1	345/87

	Issue Date	Pages	Title	Document ID	Current OR
20	20010612	72	Electron-emitting device, electron source and image-forming apparatus as well as method of manufacturing the same	US 6246168 B1	313/495
21	20010605	22	Diffractive spatial light modulator and display	US 6243063 B1	345/94
22	20010515	44	Electron-emitting device as well as electron source and image-forming apparatus using such devices	US 6231413 B1	445/24
23	20010417	289	Method and apparatus for driving an active matrix display panel	US 6219113 B1	349/42
24	20010403	47	Systems and methods for conducting electrophysiological testing using high-voltage energy pulses to stun tissue	US 6212426 B1	600/510
25	20010327	42	Electron source substrate with low sodium upper surface	US 6208071 B1	313/495
26	20010227	58	Device and method for driving liquid crystal display apparatus	US 6195077 B1	345/99
27	20010206	46	Electron beam apparatus and method of driving the same	US 6184626 B1	315/169.1
28	20010130	62	Method of manufacturing electron-emitting device electron source and image-forming apparatus	US 6179678 B1	445/24
29	20001212	53	Electron source and image forming apparatus as well as method of providing the same with means for maintaining activated state thereof	US 6160347 A	313/545

	Issue Date	Pages	Title	Document ID	Current OR
30	20000822	46	Power supply for use in electrophysiological apparatus employing high-voltage pulses to render tissue temporarily unresponsive	US 6107699 A	307/112
31	20000523	90	Voltage output circuit and image display device	US 6067066 A	345/98
32	20000509	39	Liquid crystal device having a polymer wall on another wall and surrounding a liquid crystal region and method for fabricating the same	US 6061117 A	349/156
33	20000307	46	Electron-emitting device and electron source and image-forming apparatus using the same as well as method of manufacturing the same	US 6034478 A	315/169.1
34	20000208	46	System and method for conducting electrophysiological testing using high-voltage energy pulses to stun tissue	US 6023638 A	600/510
35	20000125	47	Method of manufacturing electron-emitting device, electron source and image-forming apparatus	US 6017259 A	445/51
36	19991214	47	Apparatus for driving display apparatus	US 6002384 A	345/95
37	19991116	41	Electron-emitting device as well as electron source and image-forming apparatus using such devices	US 5986389 A	313/310

	Issue Date	Pages	Title	Document ID	Current OR
38	19990427	30	Logic circuit for liquid crystal display having pass-transistor logic circuitry and thin film transistors	US 5898322 A	326/113
39	19990406	26	Scanning circuit and image display apparatus	US 5892495 A	345/98
40	19990202	47	Electron beam apparatus and method of driving the same	US 5866988 A	315/169.1
41	19981229	44	Method of manufacturing electron-emitting device, electron source and image-forming apparatus	US 5853310 A	445/24
42	19981201	38	Liquid crystal display with back-light control function	US 5844540 A	345/102
43	19981020	12	Method for detecting defects in an active matrix liquid crystal display panel	US 5825196 A	324/770
44	19980728	14	Method of detecting possible defect of liquid crystal panel	US 5786707 A	324/770
45	19980714	23	Display-driving voltage generating apparatus	US 5781001 A	323/267
46	19980519	21	Liquid crystal luminance adjusting apparatus	US 5754150 A	345/89
47	19980505	23	LCD driving apparatus allowing for multiple aspect resolution	US 5748175 A	345/660
48	19980505	26	Multiple value voltage output circuit and liquid crystal display driving circuit	US 5747979 A	323/349

	Issue Date	Pages	Title	Document ID	Current OR
49	19980428	23	Liquid crystal display method and apparatus for controlling gray scale display	US 5745087 A	345/89
50	19980217	19	Method for driving an active matrix substrate	US 5719590 A	345/94
51	19970930	39	Liquid crystal device and method for fabricating the same	US 5673092 A	349/86
52	19970617	44	Liquid crystal device with the retardation of the liquid crystal layer greater than .lambda./2 and a method for driving the same	US 5640259 A	349/33
53	19970415	21	Display apparatus and driving circuit for driving the same	US 5621426 A	345/95
54	19970311	58	Semiconductor device	US 5610414 A	257/99
55	19970304	35	Defect detection method and apparatus for active matrix substrate or active matrix liquid crystal panel and defect repairing method thereof	US 5608558 A	349/192
56	19970107	61	Display-integrated type tablet device	US 5592197 A	345/173
57	19970107	60	Apparatus for manufacturing electron source and image forming apparatus	US 5591061 A	445/3
58	19960730	35	Electro-optical device utilizing a liquid crystal having a spontaneous polarization	US 5541747 A	349/49

	Issue Date	Pages	Title	Document ID	Current OR
59	19950509	16	Driving circuit of a ferroelectric memory device and a method for driving the same	US 5414654 A	365/145
60	19950509	37	Drive device for driving a matrix-type LCD apparatus	US 5414443 A	345/95
61	19950321	17	Active matrix driving apparatus and an active matrix driving method	US 5400048 A	345/97
62	19910521	80	Energy conversion using high charge density	US 5018180 A	378/119
63	19910409	16	Thin film EL display panel drive circuit	US 5006838 A	345/79
64	19901009	13	Thin film el display panel drive circuit	US 4962374 A	345/79
65	19900227		Electronic addressing of ferroelectric and flexoelectric liquid crystal devices	US 4904064 A	349/37
66	19880920		Capacitive electrode configuration for liquid crystal displays	US 4772099 A	349/142
67	19880223		Effective value voltage stabilizer for a display apparatus	US 4726658 A	345/212
68	19860429	13	Liquid crystal display device and method for driving thereof	US 4586039 A	345/90
69	19841204		Segmented type liquid crystal display and driving method thereof	US 4486748 A	345/52
70	19831220		Video tone control circuit	US 4422095 A	348/606

	Issue Date	Pages	Title	Document ID	Current OR
71	19831025		DAP, LCD Device with a bias voltage	US 4411496 A	349/34
72	19830531	17	Matrix type liquid crystal display	US 4386352 A	345/92
73	19830524		Segmented type liquid crystal display and driving method thereof	US 4385292 A	345/53
74	19820330		Method of driving electrochromic display device and electrochromic display device therefor	US 4322133 A	359/267
75	19820119		Driving technique for electrochromic displays of the segmented type including means for detecting a change in the display state of the segments thereof	US 4312000 A	345/105
76	19800701		Complete bleaching of non-selected display electrodes in an electrochromic display drive	US 4210909 A	345/49
77	19800701		Uniform coloration control in an electrochromic display of the segmented type	US 4210907 A	345/105
78	19751230		Method of forming colored oxide film on aluminum or aluminum alloy material	US 3929593 A	205/105
79	19750826		Drive system for liquid crystal display units	US 3902169 A	345/54

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
1	20030911	33	Non-volatile semiconductor memory device	US 20030169630 A1	365/200	
2	20030522	11	Data voltage current drive amoled pixel circuit	US 20030095087 A1	345/82	
3	20030102	12	Liquid crystal display and method for driving the same	US 20030001812 A1	345/94	
4	20021017	59	Shift register and liquid crystal display using the same	US 20020149318 A1	315/169.1	315/169.2; 315/169.3
5	20021003	146	Apparatus for initial power control for spread-spectrum communications	US 20020141478 A1	375/130	
6	20020912	22	Frame rate controller	US 20020126083 A1	345/98	
7	20020523	146	Method for initial power control for spread-spectrum communications	US 20020061050 A1	375/141	370/342
8	20020516	147	Apparatus for adaptive forward power control for spread-spectrum communications	US 20020057659 A1	370/335	370/347
9	20020502	146	Median weighted tracking for spread-spectrum communications	US 20020051482 A1	375/141	370/342
10	20020502	146	Method for using rapid acquisition spreading codes for spread-spectrum communications	US 20020051434 A1	370/335	370/342
11	20020418	146	Centroid tracking for spread-spectrum communications	US 20020044539 A1	370/335	370/503
12	20020328	147	Adaptive vector correlator for spread-spectrum communications	US 20020036998 A1	370/342	375/140
13	20020328	147	Initial power control for spread-spectrum communications	US 20020036996 A1	370/335	370/342; 455/522

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
14	20020321	147	Apparatus for adaptive reverse power control for spread-spectrum communications	US 20020034169 A1	370/335	370/342
15	20020321	147	Method for adaptive forward power control for spread-spectrum communications	US 20020034167 A1	370/329	370/252
16	20020307	147	Method for adaptive reverse power control for spread-spectrum communications	US 20020027946 A1	375/130	375/141
17	20020221	147	System for using rapid acquisition spreading codes for spread-spectrum communications	US 20020021686 A1	370/342	375/140
18	20010913	51	Liquid crystal display device and manufacturing method thereof	US 20010020988 A1	349/54	
19	20010628	16	Disk sensor power saving system	US 20010005891 A1	713/300	
20	20030909	24	Display apparatus having functions of displaying video signals as enlarged/thinned pictures	US 6618032 B1	345/89	345/100; 345/581; 345/98; 345/99; 348/580; 348/581; 348/582; 348/583
21	20030701	12	LCD panel and LCD device equipped therewith	US 6587089 B1	345/99	345/87
22	20020924	145	Adaptive vector correlator using weighting signals for spread-spectrum communications	US 6456608 B1	370/335	370/342; 375/150
23	20020820	19	Integrated circuits for testing a display array	US 6437596 B1	324/770	

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
24	20011225	16	Liquid crystal display	US 6333729 B1	345/98	345/100; 345/88
25	20011030	21	Driving method and circuit for pixel multiplexing circuits	US 6310594 B1	345/90	345/100; 345/214; 345/98
26	20010911	28	Method and circuit for driving display device	US 6288697 B1	345/87	345/94
27	20010424	31	Nonvolatile semiconductor storage device having buried electrode within shallow trench	US 6222769 B1	365/185.17	257/288; 257/316; 257/E21.69; 365/185.03
28	20010403	13	Method and apparatus for eliminating crosstalk in active matrix liquid crystal displays	US 6211851 B1	345/89	345/58
29	20001031	12	Active matrix liquid crystal display incorporating pixel inversion with reduced drive pulse amplitudes	US 6140990 A	345/92	345/94; 345/98; 345/99
30	20000912	30	Method and circuit for driving liquid crystal panel	US 6118421 A	345/89	345/96
31	20000801	39	Ferroelectric memory used for the RFID system, method for driving the same, semiconductor chip and ID card	US 6097622 A	365/145	365/149; 365/210
32	20000523	35	Programmable pulse generator	US 6067648 A	714/718	327/170; 327/172; 327/175; 327/176; 327/291
33	20000321	11	Liquid crystal display	US 6040828 A	345/213	345/212
34	20000321	20	Active matrix display with integrated drive circuitry	US 6040812 A	345/89	345/691

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
35	20000307	31	Nonvolatile semiconductor storage device having buried electrode within shallow trench	US 6034894 A	365/185.17	257/288; 257/316; 257/E21.69; 365/185.03
36	19991221	11	Method for driving a thin film transistor liquid crystal display device using varied gate low levels	US 6005542 A	345/92	345/96
37	19991109	19	Method to determine pixel condition on flat panel displays using an electron beam	US 5982190 A	324/770	324/501
38	19990817	13	Method and apparatus for eliminating crosstalk in active matrix liquid crystal displays	US 5940057 A	345/89	345/58
39	19981020	13	Driving device and driving method for a thin film transistor liquid crystal display	US 5825343 A	345/94	345/204; 345/208; 345/92
40	19980428	27	Digital printer using two-dimensional, full frame light valve	US 5745156 A	347/256	345/102; 347/255
41	19971125	51	Liquid crystal display device and method for driving the same	US 5691783 A	349/48	345/92; 349/158; 349/172; 349/37; 349/38
42	19970930	39	Driving circuit for display apparatus	US 5673061 A	345/89	345/690; 345/98
43	19961231	23	Switched capacitor analog circuits using polysilicon thin film technology	US 5589847 A	345/98	257/E21.703; 257/E27.111; 345/87
44	19960813	17	Array tester for determining contact quality and line integrity in a TFT/LCD	US 5546013 A	324/770	324/158.1

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
45	19960213	23	Thin-film structure with dense array of binary control units for presenting images	US 5491347 A	257/59	257/72; 257/88; 257/89; 349/43; 349/49
46	19930713	41	High fidelity speech encoding for telecommunications systems	US 5228076 A	379/93.17	379/388.02; 379/93.08; 704/266
47	19920922	41	Integrated communications system	US 5150357 A	370/354	379/93.15
48	19900213	29	Time dispersal encryption of TV signals	US 4901349 A	380/213	380/218; 380/221; 380/240
49	19890523	65	Computer driver module for master interface to communication and control network	US 4833600 A	709/250	
50	19890131	50	Brain learning and recognition emulation circuitry and method of recognizing events	US 4802103 A	706/38	382/157; 706/20; 706/25; 706/30
51	19880920	40	Brain emulation circuit with reduced confusion	US 4773024 A	706/20	382/157; 706/26; 706/30
52	19850430	92	Method and apparatus for processing color video signals for recording and reproducing	US 4514769 A	386/40	
53	19841016	242	Television signal disc drive recorder	US 4477847 A	360/60	386/125; 386/94
54	19820914	222	Digital data rate corrector and time base corrector	US 4349832 A	348/500	375/365; 386/20
55	19810526	194	Video frame storage recording and reproducing apparatus	US 4270150 A	386/86	360/22; 360/63; 360/78.04; 386/92

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
56	19800101	27	High bit rate digital data signal transmission system	US 4181817 A	714/820	360/41; 375/239
57	19790320	123	Television subcarrier phase correction for color field sequencing	US 4145704 A	386/21	
58	19781024	33	Precision phase controlled clock for sampling television signals	US 4122487 A	348/539	327/156; 327/94; 331/20; 331/25; 386/13
59	19781024	31	Clock signal generator providing non-symmetrical alternating phase intervals	US 4122478 A	348/505	348/509; 348/537; 386/1
60	19781024	46	Method and apparatus for inserting synchronizing words in a digitalized television signal data stream	US 4122477 A	348/497	348/476; 348/521
61	19781010	44	Apparatus for inserting a digital sync word, phase-synchronized to the color subcarrier, in place of the horizontal sync signal	US 4119999 A	386/12	348/486; 348/505
62	19780620	111	Distorted two frequency coded data interpreting method and apparatus	US 4096378 A	235/462.28	235/462.49; 360/43
63	19770222	31	Monitoring system for vehicles	US 4009375 A	455/517	340/988; 455/345; 455/99; 701/33
64	19740305	115	PASSENGER ENTERTAINMENT/PASSENGER SERVICE AND SELF-TEST SYSTEM	US 3795771 A	370/242	340/825.24; 381/82; 381/86; 725/76
65	19710720	9	INTERLACED ELECTRONIC COMMUTATOR HAVING PLURAL SUBCOMMUTATORS	US 3594725 A	370/386	327/411

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	17339	pixel adj electrode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	99839	(signal adj lines) or (data adj lines) or (column adj lines)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	49677	(common adj electrode) or (counter adj electrode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	90373	grayscale or (gray adj scale) or greyscale or (grey adj scale) or gradation or halftone or half-tone	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	131985	(phase adj modulation) or (phase adj shift$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	600	(dot adj inversion) or (column adj inversion)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	31983	driving near method	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	636	345/55.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	4168	(345/87-90).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	2232	(345/92-96).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	260	(345/101).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	117	(349/72).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	820	(345/690-696).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	18	(345/54).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	1432	(345/204).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
16	BRS	L16	530	(345/208-209).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L17	8381	9 or 10 or 11 or 12 or 13 or 14 or 15 or 16	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L18	8938	8 or 17	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
19	BRS	L19	7	2 and 3 and 4 and 5 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L20	47270	(pulse adj width adj modulation) or PWM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L21	27	5 and 20 and 7 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
22	BRS	L22	4165	((active adj matrix)near LCD) or AMLCD or TFTLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
23	BRS	L23	20267	(scanning adj lines)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
24	BRS	L24	142	2 and 5 and 23	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
25	BRS	L25	51	18 and 24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
26	BRS	L26	139	2 and 3 and 4 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
27	BRS	L27	87	18 and 26	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
28	BRS	L28	6244	(selection adj period) or (horizontal adj period)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
29	BRS	L29	263	22 and 23	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
30	BRS	L30	6	20 and 29	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
31	BRS	L31	6	5 and 6 and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
32	BRS	L32	33217	gate adj voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33	BRS	L33	1	22 and 32 and 23 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
34	BRS	L34	4	(scanning adj period) and 5 and 6 and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
35	BRS	L35	63071	source adj voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
36	BRS	L36	328	5 and 32 and 35	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
37	BRS	L37	30	20 and 36	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
38	BRS	L38	4	20 and 22 and 32 and 35	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
39	BRS	L39	1108	2 and 3 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
40	BRS	L40	22	4 and 22 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
41	BRS	L41	163	3 and 4 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
42	BRS	L42	13	22 and 41	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	L43	7	1 and 2 and 5 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
44	BRS	L44	2	5 and 6 and 20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
45	BRS	L45	12	2 and 5 and 23 and 20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
46	BRS	L46	162596	transistor and resistance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
47	BRS	L47	28	20 and 22 and 46	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
48	BRS	L48	5491	46 and 20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
49	BRS	L49	583	32 and 48	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
50	BRS	L50	52	5 and 49	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
51	BRS	L51	11496	(ON or OFF) and 46 and 32	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
52	BRS	L52	2120	51 and 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
53	BRS	L53	44	51 and 2 and 20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
54	BRS	L54	33217	(gate adj voltage) and 32	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
55	BRS	L55	14679	32 and 46	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
56	BRS	L56	0	(resistance near ON) and TFTLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
57	BRS	L57	0	(resistance near ON) and TFT	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
58	BRS	L58	4	(resistance near ON)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
59	BRS	L59	2452	TFT and 32	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
60	BRS	L60	72	59 and AMLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
61	BRS	L61	9954	TFT and resistance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
62	BRS	L62	689	switch$4 near ON	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
63	BRS	L63	0	61 and 62	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
64	BRS	L64	45	TFT and resistance and ON	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
65	BRS	L65	1573	61 and 32	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
66	BRS	L66	0	gate adj voltage adj ON	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
67	BRS	L67	0	gate adj voltage adj3 ON	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
68	BRS	L68	4521	61 and pixels	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
69	BRS	L69	299	TFT near resistance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
70	BRS	L70	0	(TFT near resistance) near ON	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
71	BRS	L71	11	69 and AMLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
72	BRS	L72	236	(ON or OFF) and 69	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
73	BRS	L73	46829	gate near voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
74	BRS	L74	89	72 and 73	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
75	BRS	L75	30	4 and 74	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
76	BRS	L76	60	(TFT near resistance) and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
77	BRS	L78	0	77 and (TFTLCD or AMLCD)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
78	BRS	L77	122	vary near 73	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
79	BRS	L79	120	(resistance near var$4) and TFT	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
80	BRS	L80	33	79 and 345/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
81	BRS	L87	8295	waveforms and (timing adj diagram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
82	BRS	L88	0	79 and 87	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
83	BRS	L89	337	3 and 32 and 35	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
84	BRS	L90	8	87 and 89	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
85	BRS	L94	6	gate near on	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
86	BRS	L95	0	out adj of adj phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
87	BRS	L96	1	gate and (ON adj period)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
88	BRS	L97	65	(data adj line) and (gate adj line) and 87	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
1	20030904	12	Method of driving liquid crystal display and liquid crystal display using the driving method	US 20030164813 A1	345/101	
2	20030612	17	Method and apparatus for driving liquid crystal display	US 20030107546 A1	345/101	
3	20031209	15	Circuit for driving a liquid crystal display and method for driving the same circuit	US 6661401 B1	345/92	345/100; 345/101; 345/102; 345/103; 345/87; 345/88; 345/96; 345/97; 345/98; 345/99
4	20021217	19	Liquid crystal display (LCD) contrast control system and method	US 6496177 B1	345/101	345/87
5	20021217	18	Liquid crystal device and method for driving the same	US 6496176 B1	345/101	345/207
6	20021217	29	Liquid crystal apparatus	US 6496170 B1	345/87	345/101; 345/204; 345/206; 345/214; 345/92; 345/93; 345/94; 345/97; 345/98; 345/99; 349/172; 349/174; 349/33; 349/38

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
7	20021210	39	Data signal line driving circuit and image display apparatus	US 6492972 B1	345/100	345/101; 345/102; 345/103; 345/104; 345/211; 345/213; 345/98; 345/99
8	20020924	35	Liquid crystal display apparatus	US 6456266 B1	345/87	345/101; 345/210; 345/95; 345/96; 345/97; 349/61; 349/74; 378/98.8
9	20020205	38	Image data reconstructing device and image display device	US 6344850 B1	345/204	345/100; 345/101; 345/205
10	20011120	21	Control system for display panels	US 6320568 B1	345/101	257/E21.614; 257/E21.703; 257/E21.705; 257/E25.013; 257/E25.021; 257/E27.026; 257/E27.111; 257/E29.273; 257/E29.275; 257/E29.282; 257/E29.295; 345/90; 345/96

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
11	20011106	13	Liquid crystal display	US 6313830 B1	345/204	327/530; 345/100; 345/101; 345/212; 345/87; 345/95; 345/98
12	20010626	7	Compensation process for a disturbed capacitive circuit and application to matrix display screens	US 6252566 B1	345/55	345/100; 345/101; 345/99
13	20000919	26	Control system for display panels	US 6121950 A	345/101	257/E21.614; 257/E21.703; 257/E21.705; 257/E25.013; 257/E25.021; 257/E27.026; 257/E27.111; 257/E29.273; 257/E29.275; 257/E29.282; 257/E29.295; 257/E33.068; 345/98
14	20000919	16	Method and apparatus for automatically maintaining a predetermined image quality in a display system	US 6121949 A	345/101	345/214; 345/618
15	19990810	25	Color liquid crystal display apparatus and method for driving the same	US 5936604 A	345/101	345/87; 345/88
16	19990720	13	Common electrode voltage driving circuit for a liquid crystal display	US 5926162 A	345/101	345/90; 345/94
17	19981222	NA	Color liquid crystal display device	US 5852430 A	345/101	345/88
18	19980505	NA	Liquid crystal display	US 5748171 A	345/101	345/88

	Issue Date	Pages	Title	Document ID	Current OR	Current XRef
19	19971202	11	Liquid crystal integrated circuit display including as arrangement for maintaining the liquid crystal at a controlled temperature	US 5694147 A	345/101	345/87; 349/42; 349/72
20	19970923	40	Method and apparatus for compensating crosstalk in liquid crystal displays	US 5670973 A	345/58	345/100; 345/101
21	19970429	13	Flat panel convergence circuit	US 5625373 A	345/58	345/100; 345/101
22	19960206	28	Method and apparatus for dynamically and adjustably generating active matrix liquid crystal display gray level voltages	US 5489918 A	345/89	345/101; 345/690
23	19931005	56	Half tone liquid crystal display circuit with an A.C. voltage divider for drivers	US 5250937 A	345/89	345/101; 345/94
24	19921117	5	Liquid crystal display with a fast warm up	US 5164849 A	349/72	345/101; 345/102; 349/161; 349/61

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	267723	LCD or (liquid adj crystal adj display)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	30817	(active adj matrix) or AMLCD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	51701	TFT or (thin adj film adj transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	29909	1 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	15403	2 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	17339	pixel adj electrode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	9485	(potential or voltage) and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	7145	7 and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	636	345/55.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	3908	(345/87-89).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	2531	(345/90-96).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	530	(345/208-209).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	6892	9 or 10 or 11 or 12	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	900	8 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	747	345/92.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
16	BRS	L16	257	14 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L17	28782	(signal adj lines) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L18	31	(polarity adj inver$4) and 17 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
19	BRS	L19	120665	phase adj shift$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L20	47270	PWM or (pulse adj width adj modulation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L21	49677	(common adj electrode) or (counter adj electrode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
22	BRS	L22	7	(conduction adj period) and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
23	BRS	L23	0	(on near state) and 6 and (4 or 5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
24	BRS	L24	231294	gradation or greyscale or grayscale or tone or halftone	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
25	BRS	L25	1568	7 and 24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
26	BRS	L27	17703	(drive adj pulse) or (applied adj voltage) and (19 or 20)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
27	BRS	L28	257	8 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
28	BRS	L29	79	24 and 28	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
29	BRS	L30	290	6 and 15	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
30	BRS	L31	83	24 and 30	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
31	BRS	L32	65	24 and 30	USPAT; EPO; JPO; DERWENT; IBM_TDB
32	BRS	L33	79	16 and 24	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33	BRS	L34	61	24 and 28	USPAT; EPO; JPO; DERWENT; IBM_TDB
34	BRS	L35	6	6 and 19 and 15	USPAT; EPO; JPO; DERWENT; IBM_TDB
35	BRS	L36	1	(conduction adj period) and 13 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
36	BRS	L37	772	(power near reduc$4) and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
37	BRS	L38	464	21 and 37	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
38	BRS	L39	36	15 and 38	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
39	BRS	L40	33217	gate adj voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
40	BRS	L41	1283	(gate adj voltage) and 40 and 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
41	BRS	L42	220	41 and 37	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
42	BRS	L43	79	24 and 42	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	L44	73	40 and 6 and 15	USPAT; EPO; JPO; DERWENT; IBM_TDB
44	BRS	L45	93408	temperature near detect$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
45	BRS	L46	260	345/101.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
46	BRS	L47	117	349/72.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
47	BRS	L48	341	46 or 47	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
48	BRS	L50	593401	(operational adj amplifier) or amplifier or opamp	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
49	BRS	L51	1122	345/98.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
50	BRS	L52	887	345/100.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
51	BRS	L53	2529	345/87.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
52	BRS	L54	4026	51 or 52 or 53	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
53	BRS	L55	23117	level adj shift$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
54	BRS	L56	11	46 and 55	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
55	BRS	L57	413	54 and 55	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
56	BRS	L58	622	54 and 50	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
57	BRS	L59	6	45 and 50 and 51	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
58	BRS	L60	262	50 and 51	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
59	BRS	L61	6	45 and 60	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
60	BRS	L62	2818	(column adj driver) or (scanning adj driver)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
61	BRS	L63	218	55 and 62	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
62	BRS	L64	0	46 and 63	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
63	BRS	L65	159	55 and 62	USPAT; EPO; JPO; DERWENT; IBM_TDB
64	BRS	L66	24	2 and 46	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB